"Theory and Applications of Computationally Universal Metabolic P Systems" 3rd-year presentation

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Section 1

Introduction

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$\textbf{Electrical Circuits}\leftrightarrows \textbf{Metabolism}$

or find a bidirectional transformation between electrical circuits and metabolism.

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Electrical Circuits 5 Metabolism

Where does the inspiration come from?

- Terje Lomø's long term potentiation [5];
- Kidney loops and mechanical engineering [4, p. 75];
- Miguel Nicolelis' experiment with monkeys and virtual arms.

Electrical Circuits \leftrightarrows **Metabolism**

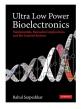
Is it a sound?

- Both are dynamical systems;
- Several living-beings components are modeled after engineering concepts:
 - Circulatory systems ⇔ fluid mechanics;
 - Skeleton ⇔ solid mechanics;
 - Muscular moviment ⇔ electricity;
 - • •
- Correlated to:
 - Biomedical engineering;
 - Systems biology;
 - Synthetic biology
- A just-born research field.

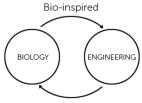
Once upon a time...

Electrical Circuits \leftrightarrows **Metabolism**





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Analysis, Instrumentation, Design, Repair

Electrical Circuits ← Metabolism

- Get a specification of a metabolism;
- Transform it in a specification of an electrical circuit;
- Automatically generate an electrical circuit;
- Reproduce the metabolic behavior in electrical circuit;
- Tune the behavior in the generated electrical circuit.¹

Systems Biology.

¹Bonus feature.

Electrical Circuits \rightarrow Metabolism

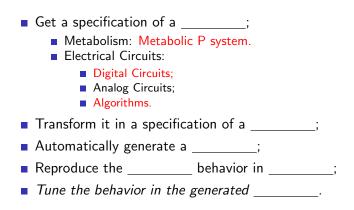
- Get a specification of an electrical circuit;
- Transform it in a specification of a metabolism;
- Automatically generate a metabolism;
- Reproduce the electrical circuit behavior in metabolism;

Synthetic Biology.

Electrical Circuits 5 Metabolism

- Get a specification of a _____;
- Transform it in a specification of a _____;
- Automatically generate a _____;
- Reproduce the _____ behavior in _____;
- Tune the behavior in the generated ______.

Electrical Circuits Metabolism



Electrical Circuits Metabolism

- Get a specification of a ; Metabolism: Metabolic P system. Electrical Circuits: Digital Circuits; Analog Circuits; Algorithms. Transform it in a specification of a ; Theoretical (core) work of the PhD thesis. Automatically generate a _____; Reproduce the behavior in ;
- Tune the behavior in the generated ______.

Electrical Circuits \leftrightarrows **Metabolism**

- Get a specification of a ; Metabolism: Metabolic P system. Electrical Circuits: Digital Circuits; Analog Circuits: Algorithms. Transform it in a specification of a ; Theoretical (core) work of the PhD thesis. Automatically generate a ; Practical application of the PhD research. Reproduce the _____ behavior in ____;
- Tune the behavior in the generated ______.

Electrical Circuits Metabolism

- Get a specification of a _____; Metabolism: Metabolic P system. Electrical Circuits: Digital Circuits; Analog Circuits; Algorithms. Transform it in a specification of a ; Theoretical (core) work of the PhD thesis. Automatically generate a _____; Practical application of the PhD research. Reproduce the behavior in ; Validation of the PhD work.
 - Tune the behavior in the generated ______.

Electrical Circuits Metabolism

 Get a specification of a ; Metabolism: Metabolic P system. Electrical Circuits: Digital Circuits; Analog Circuits: Algorithms. Transform it in a specification of a ; Theoretical (core) work of the PhD thesis. Automatically generate a ; Practical application of the PhD research. Reproduce the _____ behavior in _____; Validation of the PhD work. Tune the behavior in the generated Users's application.

Theory

- How can I represent metabolism?
- 2 How can I represent circuit?
- 3 Can I map every metabolism to circuit?
- 4 Can I map every circuit to metabolism?
- 5 What is the map procedure? (Both.)
- 6 Do I have restrictions?
- Is the mapping optimal? (In which sense?)

Practice

- Instance of a metabolism as an electrical circuit.
- 2 Instance of an electrical circuit as a metabolism.
- 3 Automatic mapping of metabolism to electrical circuit.
- Automatic mapping of electrical circuit to metabolism.

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Section 2

Basic Knowledge

• To understand the work, it is required to have in mind two concepts:

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- 1 Metabolic P systems;
- 2 Computationally Universal Devices.
- The rest of the work is self-contained.

Metabolic P systems

Static

$$G = (M, R, I, \Phi)$$

- set of substances M;
- set of rules R;
- initial state I;
- set of fluxes Φ.

Dynamic

$$\mathcal{M} = (G, \tau, \mu, \nu)$$

- Metabolic P grammar G;
- Period of the dynamics, τ ;
- Number of conventional mole µ;
- Vector of mole masses ν ;
- Update recurrent equation (Equational Metabolic Algorithm).

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Static

 $G = (M, R, I, \Phi)$

- set of substances M;
- set of rules R;
- initial state I;
- set of fluxes Φ.

Dynamic

 $\mathcal{M} = (G, \tau)$

- Metabolic P grammar G;
- Period of the dynamics, τ ;
- Update recurrent equation (Equational Metabolic Algorithm).

- Subset of P systems (membrane computing);
- Discrete dynamical system;
- Deterministic computation;
- Very mature as numerical algorithm;
- Few theoretical computer science results.

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Necessary for PhD hypothesis.

- Subset of P systems (membrane computing);
- Discrete dynamical system;
- Deterministic computation;
- Very mature as numerical algorithm;
- Few theoretical computer science results.

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Necessary for PhD hypothesis.

Computationally Universal Devices

- Computationally universal devices ⇔ Turing-complete
- Recognizes the highest level of the Chomsky-Schützenberger hierarchy

Grammar	Language	Automaton
Type-0	Recursively enumerable	Turing machine
Type-1	Context-sensitive	Linear-bounded non-deterministic Turing machine
Type-2	Context-free	Non-deterministic pushdown automaton
Type-3	Regular	Finite state automaton

- There are several computationally universal models. Register machine was picked.
 - Simple;
 - Easy to reason about;
 - von Neumann architecture-like;
 - Low-level programming.

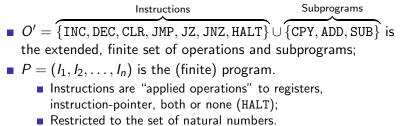
$$\mathcal{R} = (R, O, P)$$
 [9]

- R is the finite set of registers (with infinite capacity)
- $O = \{$ INC, DEC, JNZ, HALT $\}$ is the finite set of operations;
- $P = (I_1, I_2, \dots, I_n)$ is the (finite) program.
 - Instructions are "applied operations" to registers, instruction-pointer, both or none (HALT);

Restricted to the set of natural numbers.

$$\mathcal{R} = (R, O', P)$$

• *R* is the finite set of *registers* (with infinite capacity)



Section 3

Theory

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- **1 Q:** How can I represent metabolism?
 - A: Metabolic P systems.
- 2 Q: How can I represent circuit?
 - A: Analog, digital circuits or algorithms.
- **3 Q:** Can I map every metabolism to circuit?
- 4 Q: Can I map every circuit to metabolism?
- **5 Q:** What is the map procedure? (Both.)
- **6 Q:** Do I have restrictions?
- **Q:** Is the mapping optimal? (In which sense?)

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- **1 Q:** How can I represent metabolism?
 - A: Metabolic P systems.
- **2 Q:** How can I represent circuit?
 - A: Algorithms.
- **3 Q:** Can I map every metabolism MP system to circuit algorithm?
- 4 Q: Can I map every circuit algorithm to metabolism MP system?

- **5 Q:** What is the map procedure? (Both.)
- 6 Q: Do I have restrictions?
- **Q:** Is the mapping optimal? (In which sense?)

Subsection 1

$\mathsf{Algorithms} \mapsto \mathsf{Metabolic} \ \mathsf{P} \ \mathsf{systems}$

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Algorithms \mapsto Metabolic P systems

Q: Can I map every algorithm to MP system?

Algorithm

- Representation of register machine;
- Recursively enumerable language;
- Sequential execution;
- Self-reference at run time (e.g., JNZ);
- Operations $\mathbb{N} \mapsto \mathbb{N}$;
- Finite-set of operations.

Metabolic P system

- Dynamical system;
- Could be context-sensitive language [1, 8]. More ambitious attempts [7] has failed.
- Parallel execution;
- Reference to previous-state only;
- Operations $\mathbb{R} \mapsto \mathbb{R}$;
- No restriction to usage of functions.

Register machine	Metabolic P grammar
$\overline{\mathcal{R} = (R, O', P)}$	$G = (M, R, I, \Phi)$
Set of registers R	Set of metabolites M
Program (sequence) P	Set of rules R
Frogram (sequence) F	Set of fluxes Φ
Initial state of the registers	Initial state I

- Restrict MP systems to ℕ;
- Create a new class of MP systems that manage it correctly: fluxes and rule-application.

Definition (MP⁺ Grammar)

An MP⁺ grammar $G' = (M, R, I', \Phi')$ is a derivation from a standard MP grammar $G = (M, R, I, \Phi)$ if its vector of initial values for substances I' has all components greater than or equal to zero, the set of consuming fluxes of the metabolite x defined as $\Phi'_x^- = \{\varphi'_j : \text{mult}^-(x, r_j) > 0, \forall r_j \in R\}$, and G' respects the following restrictions at every computational step t_i :

1
$$\forall \varphi \in \Phi : \varphi'(t_i) = \begin{cases} \varphi(t_i) &, \text{ if } \varphi(t_i) \ge 0 \\ 0 &, \text{ otherwise} \end{cases}$$
;
2 $\sum_{\varphi' \in \Phi'_x} \varphi'(t_i) \le x(t_i); \text{ otherwise } \varphi'(t_i) = 0, \forall \varphi' \in \Phi'_x$ at the execution step t_i .

Sequential execution and self-reference:

- For each instruction I_j of program P, there will be a respective metabolite I_j representing the *instruction pointer*, active or not, at that instruction;
- For each instruction I_j of the type JZ or JNZ, there will be a respective metabolite L_j;
- To signalize the halt of operation of the device, there will be a special (fixed-point) metabolite HALT.

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Mapping Rules and Fluxes

Register machine	Metabolic P grammar
if I_j is INC(R_i)	$ \begin{array}{l} I_j \to I_{j+1} : I_j \\ \emptyset \to R_i : I_j \end{array} $
if I_j is DEC(R_i)	$ \begin{array}{c} I_j \to I_{j+1} : I_j \\ R_i \to \emptyset : I_j \end{array} $
if I_j is $JNZ(R_i, I_k)$	$ \begin{array}{c} I_j \rightarrow L_j : I_j \\ L_j \rightarrow I_k : L_j - I_{j+1} \\ L_j \rightarrow \emptyset : I_{j+1} \\ \emptyset \rightarrow I_{j+1} : I_j - R_i \end{array} $
if I_j is HALT	$I_j \rightarrow HALT : I_j$

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At begining, $I_1 = 1$; HALT + $\sum_{j=1}^{p} l_j = 1$

•
$$0 \leq \sum_{L_j \in M} L_j \leq 1$$

Mapping Rules and Fluxes

Register machine	Metabolic P grammar
if I_j is INC(R_i)	$egin{array}{llllllllllllllllllllllllllllllllllll$
if I_j is DEC(R_i)	$I_j o I_{j+1} : I_j$ $R_i o \emptyset : I_j$
if I_j is $JNZ(R_i, I_k)$	$ \begin{array}{c} I_j \rightarrow L_j : I_j \\ L_j \rightarrow I_k : L_j - I_{j+1} \\ L_j \rightarrow \emptyset : I_{j+1} \\ \emptyset \rightarrow I_{j+1} : I_j - R_i \end{array} $
if I_j is HALT	$I_j \rightarrow HALT : I_j$

At begining, $I_1 = 1$; $HALT + \sum_{j=1}^{p} I_j = 1$ $0 \le \sum_{L_j \in M} L_j \le 1$

Theorem (Translation of Register Machine to MP^+)

For any register machine \mathcal{R} exists an equivalent positively controlled MP grammar \mathcal{G}_+ .

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The Proof I

Given a register machine $\mathcal{R} = (R, I, P)$ with |R| = r and |P| = p, a positively controlled MP grammar $\mathcal{G}_+ = (M, Ru, I, \Phi)$ is constructed

- 1 adding a metabolite R_i in the set M for each register $R_i \in R$;
- 2 adding a metabolite I_i in the set M for each of the instructions in $I_i \in P$;
- 3 adding a metabolite L_i in the set M for each instruction $I_i \in P$ of the type JNZ;
- 4 adding a HALT metabolite in the set M;
- 5 defining the initial state of the metabolites R_j equal to the initial values of the registers R_j, the initial values of all the other metabolites to 0 and the initial value of I₁ to 1;

6 adding the rules to Ru and the fluxes to Φ according to the following rules:

From the rules above, it is possible to notice that I_j and L_j instructions controls the execution flow of the system and satisfies

$$HALT + \sum_{j=1}^{p} l_j = 1$$

$$0 \leq \sum_{L_j \in M} L_j \leq 1$$

ensuring no two instructions are executed at the same time, but its execution starts from instruction l_1 and proceeds sequentially (or jumps to another one in case of a satisfying JNZ instruction). All operations are mappings from and to the \mathbb{N} set once both \mathcal{R} and \mathcal{G}_+ , by definition, restrict their operations to this set.

At last, when a rule $I_i \rightarrow HALT$ is performed, the system is stuck in a fixed point since there is no rules for

"exiting" this state.

The Side-Effect Prize

- Result of the transformation is a very simple MP system—MP⁺V ;
- Not only *simple*, but equivalent to register machine ⇒ computationally universal;
- But MP⁺V ⊂ MP⁺ ⊂ MP and MP⁺V is computationally universal ⇒ MP is computationally universal!

Definition (MP⁺V Grammar)

An MP⁺V grammar $G = (M, R, I, \Phi)$ is a MP⁺ one in which:

∀r ∈ R and v', v'' ∈ M, r must have one of the following shapes:
 Ø → v'';
 v' → Ø; or
 v' → v'';
 ∀φ ∈ Φ and m', m'' ∈ M, the flux has either the form φ = m' or φ = m' - m''.

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The Computational Universality of Metabolic Computing

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May 12, 2015

Abstract

System and synthetic biology are rapidly evolving systems, but both lack tools such as those used in engineering environments to shift the their focus from the design of parts (details) to the design of systems (behaviors); to aggravate, there are insufficient theoretical justifications on the computational limits of biological systems. To diminish these deficiencies, we present theoretical results over the Turing-equivalence of metabolic systems, defines rules for translations of algorithms into metabolic P systems and presents a software tool to assist the task in an automatic way.

Figure: arXiv:1505.02420 [3]

Subsection 2

Metabolic P systems \mapsto Algorithms

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- According to previous theorem, yes!
- Even using register machines and MP⁺V , there are some complications:
 - 1 unordered application of rules
 - 2 parallel application of rules

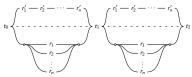


Figure: Graphical representation of the block of execution. [2]

3 positivity control

- According to previous theorem, yes!
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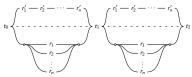


Figure: Graphical representation of the block of execution. [2]

3 positivity control

- According to previous theorem, yes!
- Even using register machines and MP⁺V , there are some complications:

- 1 application of rules
- 2 positivity control

Solution 1

Command block and/or Monad

- According to previous theorem, yes!
- Even using register machines and MP⁺V , there are some complications:

- 1 application of rules
- 2 positivity control

Solution 2

Inclusion of a subprogram

Runtime of a Computational Step

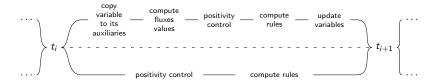


Figure: Representation of a computation step MP⁺V systems (lower part) and its equivalent register machine (upper part). [2]

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Pseudo-code of $MP^+V \mapsto Register Machine$

```
while R_{HALT} = 0 do
     for all variable v \in M do
                                               copy variables to auxiliaries
          R_{u'} \leftarrow R_{u}
     end for
     for all flux \varphi \in \Phi do
                                                                     ▷ compute fluxes
          R_{\omega} \leftarrow \varphi(t_i)
     end for
     for all variable v \in M do
                                                     positivity control property
          for all flux \varphi_{\nu}^{-} \in \Phi_{\nu}^{-} do
               R_{sum} \leftarrow R_{sum} + R_{o}
          end for
          if R_{sum} > v then
               for all flux \varphi_{\nu}^{-} \in \Phi_{\nu}^{-} do
               R_{\varphi_v^-} \leftarrow 0
end for
          end if
     end for
     for all rule r do
                                                                      compute rules
          if r is of the form \emptyset \to v : \varphi then
               R_{v'} \leftarrow R_{v'} + \varphi
          else if r is of the form v \to \emptyset : \varphi then
               R_{\nu'} \leftarrow R_{\nu'} - \varphi
          else
                             \triangleright hence, it must be of the form v_1 \rightarrow v_2; \varphi
               R_{v'_1} \leftarrow R_{v'_1} + \varphi
               R_{v_2'} \leftarrow R_{v_2'} - \varphi
          end if
     end for
     for all variable v \in M do
                                                                    update variables
          R_{u} \leftarrow R_{u'}
     end for
end while
```

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Rules are Easy. . .

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ightarrow$

$$\begin{split} \emptyset &\rightarrow V_1: \varphi & 1 \quad \text{ADD}(R_{V_1}, R_{\varphi}, R_{aux}) \\ 2 \quad \text{CPY}(R_{aux}, R_{V_1}) \\ V_1 &\rightarrow \emptyset: \varphi & 1 \quad \text{SUB}(R_{V_1}, R_{\varphi}, R_{aux}) \\ 2 \quad \text{CPY}(R_{aux}, R_{V_1}) \\ 1 \quad \text{SUB}(R_{V_1}, R_{\varphi}, R_{aux}) \\ 2 \quad \text{CPY}(R_{aux}, R_{V_1}) \\ 1 \quad \text{SUB}(R_{V_2}, R_{\varphi}, R_{aux}) \\ 2 \quad \text{CPY}(R_{aux}, R_{V_1}) \\ 3 \quad \text{ADD}(R_{V_2}, R_{\varphi}, R_{aux}) \\ 4 \quad \text{CPY}(R_{aux}, R_{V_2}) \\ 1 \quad \text{JNZ}(R_{HALT}, 3) \\ 2 \quad \text{JMP}(4) \\ 3 \quad \text{HALT} \\ 4 \quad \text{SUB}(R_{V_1}, R_{\varphi}, R_{aux}) \\ 5 \quad \text{CPY}(R_{aux}, R_{V_1}) \\ 6 \quad \text{ADD}(R_{HALT}, R_{\varphi}, R_{aux}) \\ 7 \quad \text{CPY}(R_{aux}, R_{HALT}) \end{split}$$

... The Surroundings Aren't!

- There to ensure the proper/correct execution;
- Hidden dynamics of the system;
- 80% of the process, most of the generated source code;
- Processes:
 - 1 Copy variables values to auxiliaries registers;
 - 2 Compute fluxes values for current computational step;

- 3 Perform *positivity control* on every rule;
- 4 Update the variables values with computed ones;
- **5** Loop the systems up to fixed-point HALT \neq 0.

Copy variables values to auxiliaries registers

Compute fluxes values for current computational step

Update the variables values with computed ones

 $1 \quad CPY(R_V, R_{V_{aux}})$ if $\varphi = V$ then $1 \quad CPY(R_V, R_{\varphi})$ else \triangleright Hence, $\varphi = V_1 - V_2$ $1 \quad SUB(R_{V_1}, R_{V_2}, R_{\varphi})$ end if

1
$$CPY(R_{V_{aux}}, R_V)$$

Two contrains to satisfy:

1 Fluxes must always belong to the set of positive number;

$$arphi'(t_i) = egin{cases} arphi(t_i) &, \mbox{ if } arphi(t_i) \geq 0 \\ 0 &, \mbox{ otherwise} \end{cases}$$

2 Sum of all consuming fluxes for a given variable must be smaller or equal to the amount of the variable

$$\sum_{\varphi' \in \Phi'_x} \varphi'(t_i) \le x$$

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• Two contrains to satisfy:

1 Fluxes must always belong to the set of positive number;

 $\mathbf{\subseteq} \mathsf{Condition \ satisfied \ by} \ \varphi : \mathbb{N} \mapsto \mathbb{N}$

2 Sum of all consuming fluxes for a given variable must be smaller or equal to the amount of the variable

$$\sum_{\varphi' \in \Phi'_x} \varphi'(t_i) \le x$$

• Two contrains to satisfy:

1 Fluxes must always belong to the set of positive number;

Condition satisfied by $\varphi : \mathbb{N} \mapsto \mathbb{N}$

2 Sum of all consuming fluxes for a given variable must be smaller or equal to the amount of the variable

 φ

$$\sum_{i'\in\Phi'_x}\varphi'(t_i)\leq x$$

... But MP⁺ Is Hard!

$$\begin{split} \sum_{\varphi' \in \Phi'_{x}} \varphi'(t_{i}) \begin{cases} 1 & \text{CLR}(R_{sum}) \\ 2 & \text{ADD}(R_{\varphi_{1}}, R_{sum}, R_{sum}) \\ 3 & \text{ADD}(R_{\varphi_{1}}, R_{sum}, R_{sum}) \\ 4 & \text{ADD}(R_{\varphi_{2}}, R_{sum}, R_{sum}) \\ \vdots \\ k+1 & \text{ADD}(R_{\varphi_{k}}, R_{sum}, R_{sum}) \\ k+3 & \text{JZ}(R_{comparator}) \\ k+4 & \text{JZ}(R_{comparator}, k+8) \\ k+5 & \text{DEC}(R_{sum}) \\ k+6 & \text{DEC}(R_{comparator}) \\ k+7 & \text{JMP}(k+3) \end{cases} \end{split}$$
Failure: $\forall \varphi', \varphi' \leftarrow 0 \begin{cases} k+8 & \text{CLR}(R_{\varphi_{1}}) \\ k+9 & \text{CLR}(R_{\varphi_{2}}) \\ \vdots \\ 2\cdot k+7 & \text{CLR}(R_{\varphi_{k}}) \end{cases}$

$$\sum_{\varphi'\in \Phi'_x^-}\varphi'(t_i)\leq x$$

Theorem (Translation of MP⁺V to Register Machine)

For any MP^+V grammar \mathcal{M}_+ exists an equivalent register machine \mathcal{R} .

Corollary

For any computable MP grammar \mathcal{M} exists an equivalent register machine \mathcal{R} .

Automatic Translation of MP⁺V Systems to Register Machines

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Abstract. The present work proposes a translation of MP systems into register machines. The already proved universality of MP grammars [6] and the very simple subclass derived from it are used, in here, to present a specification of the metabolic computational paradigm of MP grammars at low (register) level, which is a first step toward a circuit-based implementation of these systems.

Figure: Presented at 16th Conference on Membrane Computing (CMC16) [3].

Recalling the Guiding Questions

- **1 Q:** How can I represent metabolism?
 - A: Metabolic P systems.
- 2 Q: How can I represent circuit?
 - A: 🗹 Algorithms.
- **3 Q:** Can I map every MP system to algorithm?
 - A: Yes, since they are computable.
- Q: Can I map every algorithm to MP system?
 A: Yes.
- **5 Q:** What is the map procedure? (Both.)
 - A: Proof of theorems.
- 6 Q: Do I have restrictions?
 - A: Yes: MP must be computable.
- **Q:** Is the mapping optimal? (In which sense?)

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Section 4

Practical Applications

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- 1 Instance of a metabolism as an electrical circuit.
- 2 Instance of an electrical circuit as a metabolism.
- 3 Automatic mapping of metabolism to electrical circuit.
- 4 Automatic mapping of electrical circuit to metabolism.

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Subsection 1

Bidirectional Compiler

Bidirectional compiler:

- **1** Register machine \mapsto MP⁺V ;
- **2** MP⁺V \mapsto register machine.
- Available in three flavors:
 - Library;
 - 2 Standalone command-line application;
 - 3 Standalone web interface.
- $\approx 100\%$ coded in **X** Haskell;
 - 34 files, 1802 lines-of-code;
 - Except few Javascript, CSS and HTML code for web interface.

$Compiler \equiv Automatic \ Translation$

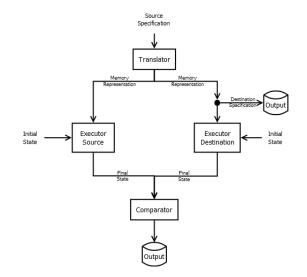


Figure: Relation among modules in the compiler.

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$Compiler \equiv Automatic \ Translation$

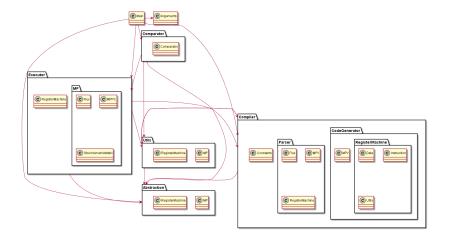


Figure: Relation among modules in the compiler.

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Live-Action!

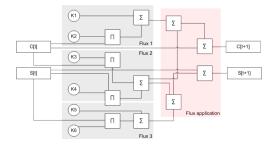
Subsection 2

Digital Circuit

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- MP system \mapsto VHDL \mapsto FPGA;
 - VHDL is algorithmic representation of the digital circuit;
 - FPGA is the digital circuit *per se*.
- Derived from a general framework discovered;
- 100% done at Vilniaus Gediminos Technikos Universitetas when in Erasmus Plus =;
 - They are starting a team on the field with a PhD student.

VHDL Hardware Implementation



-- combinational instructions should run <= run;

-- sequential instructions

```
step: process (step clock, should run, resetBook)
   variable rule 1 : ufixed (integer part length-1 downto -fractional part length);
   variable rule 2 : ufixed (integer part length-1 downto -fractional part length);
   variable rule 3 : ufixed (integer part length-1 downto -fractional part length);
   variable cosine var : ufixed (integer part length-1 downto -fractional part length);
   variable sine var : ufixed (integer part length-1 downto -fractional part length);
begin
   if (rising edge(step clock) and should run - '1') then
        if (resetBook = '1') then
           cosine <= cosine zero;
            sine <= sine zero;
        else
           rule 1 := resize(k1 + resize(k2 * cosine, rule 1), rule 1);
           rule 2 := resize(resize(k3 * cosine, rule 2) + resize(k4 * sine, rule 2), rule 2);
           rule 3 := resize(k5 + resize(k6 * sine, rule 3'high, rule 3'low), rule 3);
           cosine var := resize(cosine + rule 1 - rule 2, cosine);
           sine var := resize(sine + rule 2 - rule 3, sine);
           cosine <- cosine var;
           sine <= sine var;
        end if;
    end if:
end process step;
```

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VHDL Hardware Implementation



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Subsection 3

Discrete Fourier Transform

Discrete Fourier transform using MP power to:

1 generate periodic signals (here, sine and cosine);

2 numeric regression (LGSS).

Frenquencies:

- In a fixed-range;
- Dynamically computed using τ of MP and Nyquist frequency.

Benchmark:

- Accuracy is better than MATLAB/FFTW;
- Speed is not so promising, one order of magnitude slower;

■ MATLAB + JVM *vs.* native *divide-and-conquer* code.

Signals	Frequency	Numerical Frequency	FFT	MP-FFT	MP-FFT (MATLAB)
1	20	20	20	20	20
2	20 + df	20.5	20.5	20.5	20.5
3	$20 + \frac{3}{4} \cdot df$	20.375	20.5	$\{20, 20.5\}$	{20 20.5}
4	{20, 47}	$\{20, 47\}$	$\{20, 47.5\}$	$\{20, 47, 47.5\}$	{20 47}
5	$\{20 + df, 47 + 3 \cdot df\}\$	$\{20.5, 48.5\}$	$\{20.5, 49\}$	$\{20.5, 48.5, 49\}$	$\{20.5 \ 48.5\}$
6	$\{20 + \frac{3}{4} \cdot df, 47 + \frac{2}{3} \cdot df\}$	$\{20.375, 47.\overline{3}\}\$	$\{20.5, 47.5\}$	$\{20.5, 47.5\}$	$\{20.5 \ 47.5\}$
7	20 + noise	20 + noise	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$	20	20
8	20 + df + noise	20.5 + noise	$ \begin{array}{c} \{1, 3.5, 4.5, 6, 7.5, 10, 12, \\ 14, 15.5, 17.5, 19, 20.5, \\ 21.5, 23, 24, 25.5, 27, 28, \\ 30, 32, 33, 34.5, 36, 37.5, \\ 40, 42, 43, 44, 47, 48, 49.5\} \end{array} $	20.5	20.5
9	$20 + \frac{3}{4} \cdot df$ + noise	20.375 + noise	$ \begin{array}{l} \{1.5,\ 2.5,\ 5.5,\ 6.5,\ 8,\ 9.5,\\ 11,\ 12,\ 13,\ 15,\ 16,\ 17.5,\\ 20.5,\ 22.5,\ 23.5,\ 25.5,\ 25.5,\ 26.5,\\ 27.5,\ 28.5,\ 29.5,\ 31,\ 32,\\ 33.5,\ 34.5,\ 36,\ 37,\ 38,\ 40,\\ 42,\ 43,\ 44.5,\ 46,\ 47,\ 49\} \end{array}$	20.5	20.5

- Instance of a MP system as an electrical circuit and algorithm.
- Instance of an algorithm as a MP system.
- 3 Automatic mapping of MP system to algorithm.
- 🛽 🗹 Automatic mapping of algorithm to MP system.

Section 5

Conclusions

- MP is more sound, theoretically speaking;
 - Computationally universal, solving past pendencies [6, 7];

- Minimalistic subclass MP⁺V .
- Definition of translation procedures in both-ways.
- Practical examples, hardware and software.
- Open-field for new studies, such as optimization (of translation) and super-computation (using MP⁺V).



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